



# The Belle II upgrade program

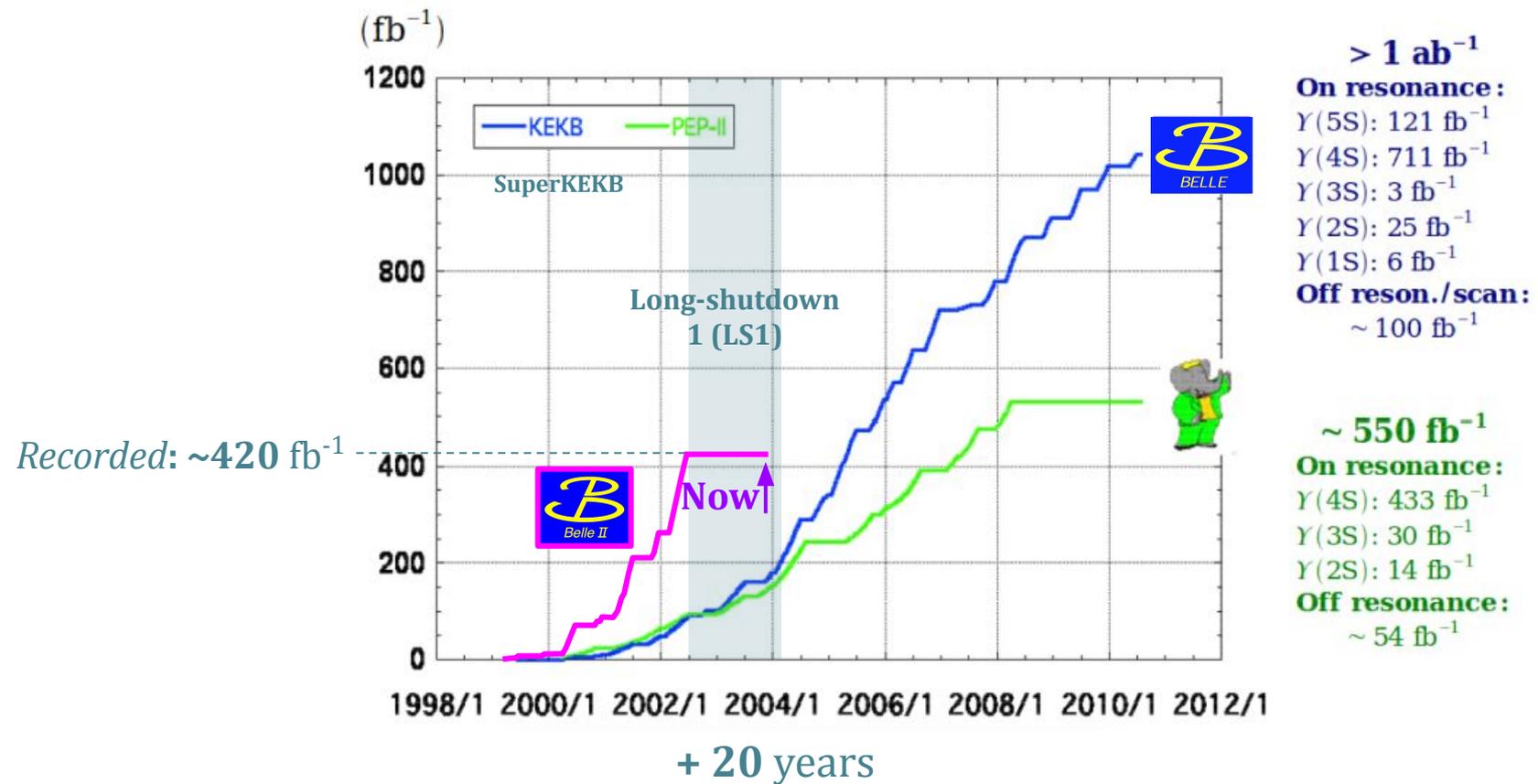
HQL2023 Mumbai

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on behalf of the Belle II collaboration

# High-precision measurements at very high luminosities



# World-record luminosity...



...but there's still a long way to go...

We need:

- **120-fold** increase in **integrated luminosity** ( $0.4 \rightarrow 50 \text{ ab}^{-1}$ ) *via...*
- **16-fold** increase in **instantaneous luminosity** ( $0.4 \rightarrow 6 \times 10^{35} \text{ cm}^{-2} \text{ s}^{-1}$ )

This is an enormous challenge for the **accelerator** and **detector**...

...and **backgrounds** are higher than anticipated

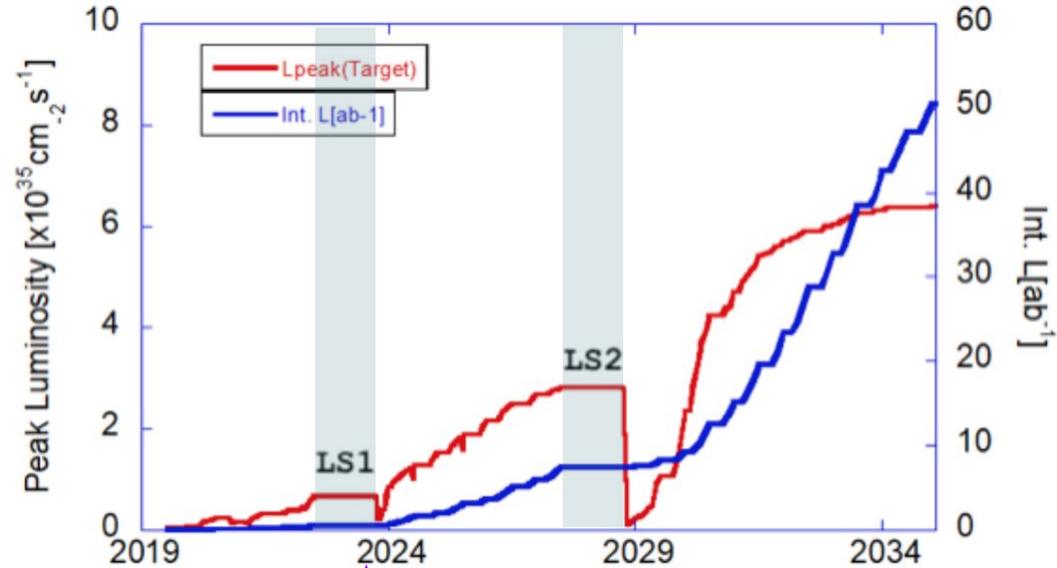


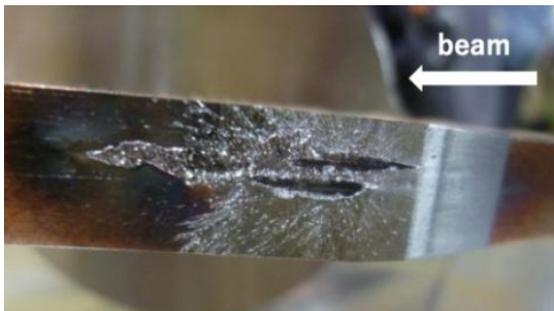
Figure 1.3: Projected luminosity for SuperKEKB.

*Here's what we're doing to meet this challenge...*

## LS1 upgrades (now)

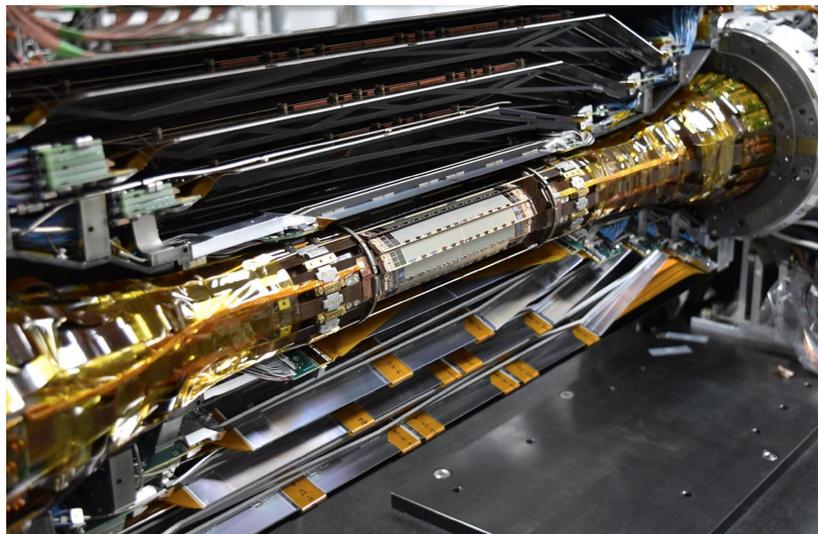
### Machine

- New beam-loss monitors
- **More-resilient collimators**
- Improved neutron shielding
- RF cavity replacement, faster kicker magnets at injector
- Operations restart in **January 2024**

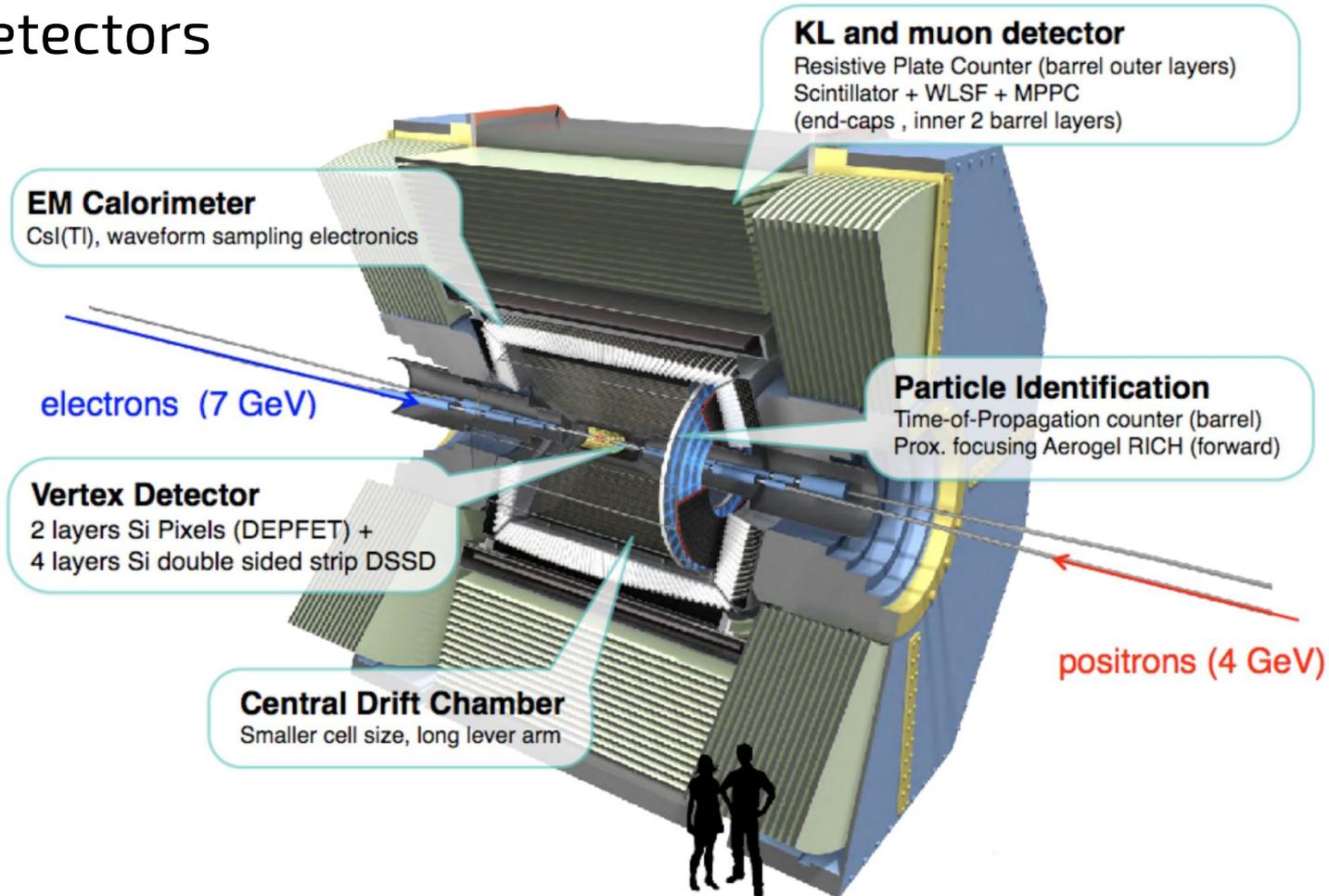


### Detector

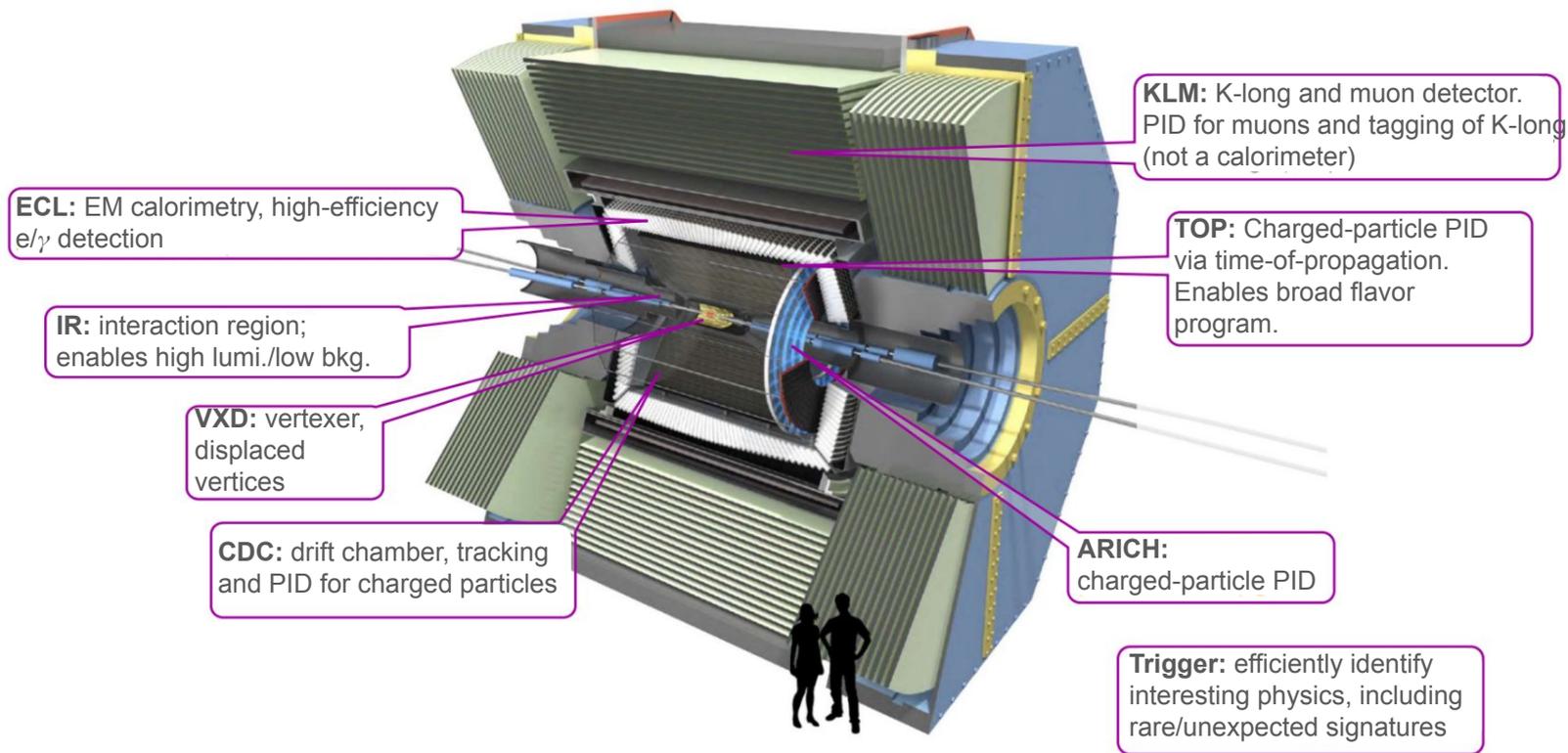
- Installation of **complete pixel detector**
- Replacement of ~50% of TOP MCP-PMs
- Improved CDC gas distribution and monitoring
- DAQ system upgrade to PCIe40



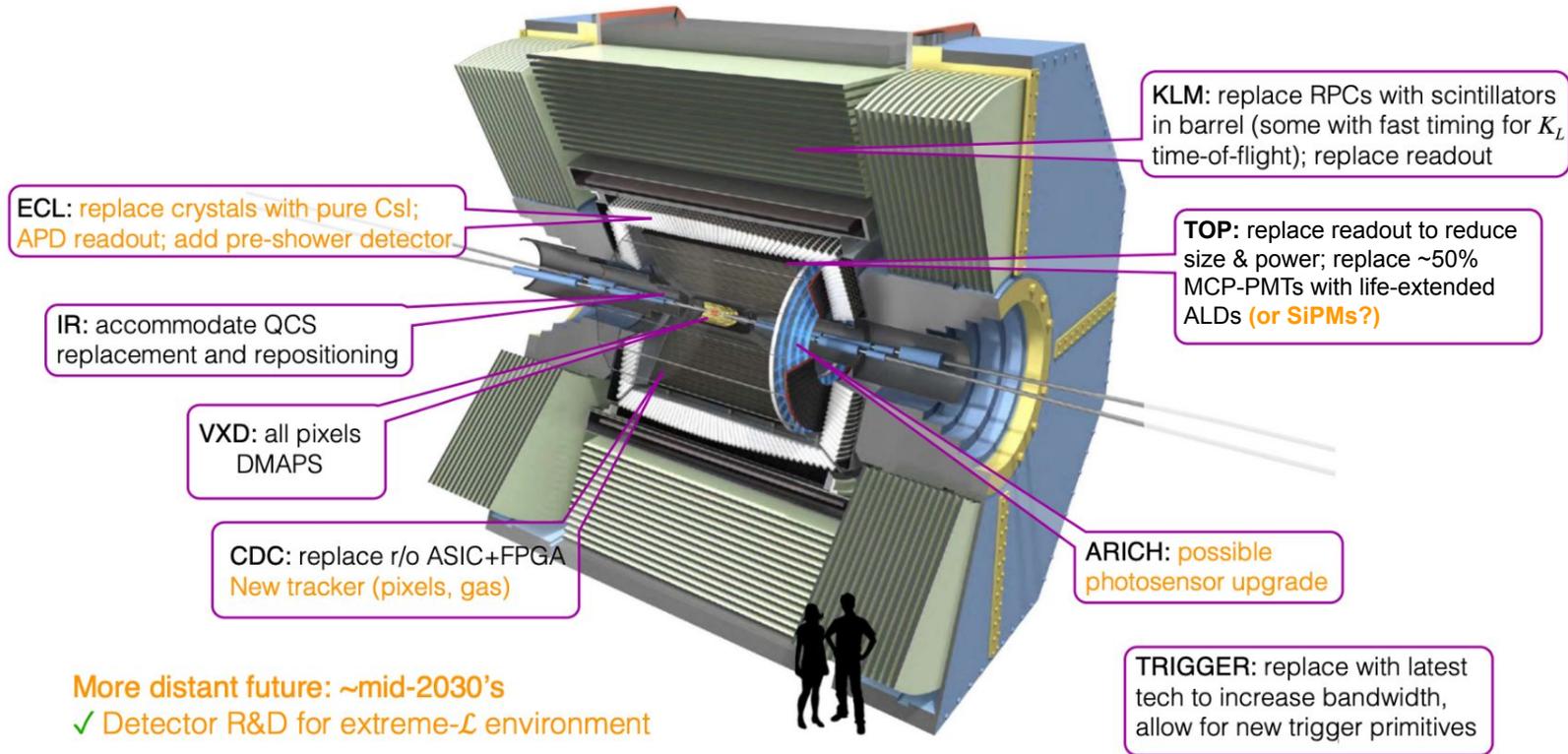
# Subdetectors



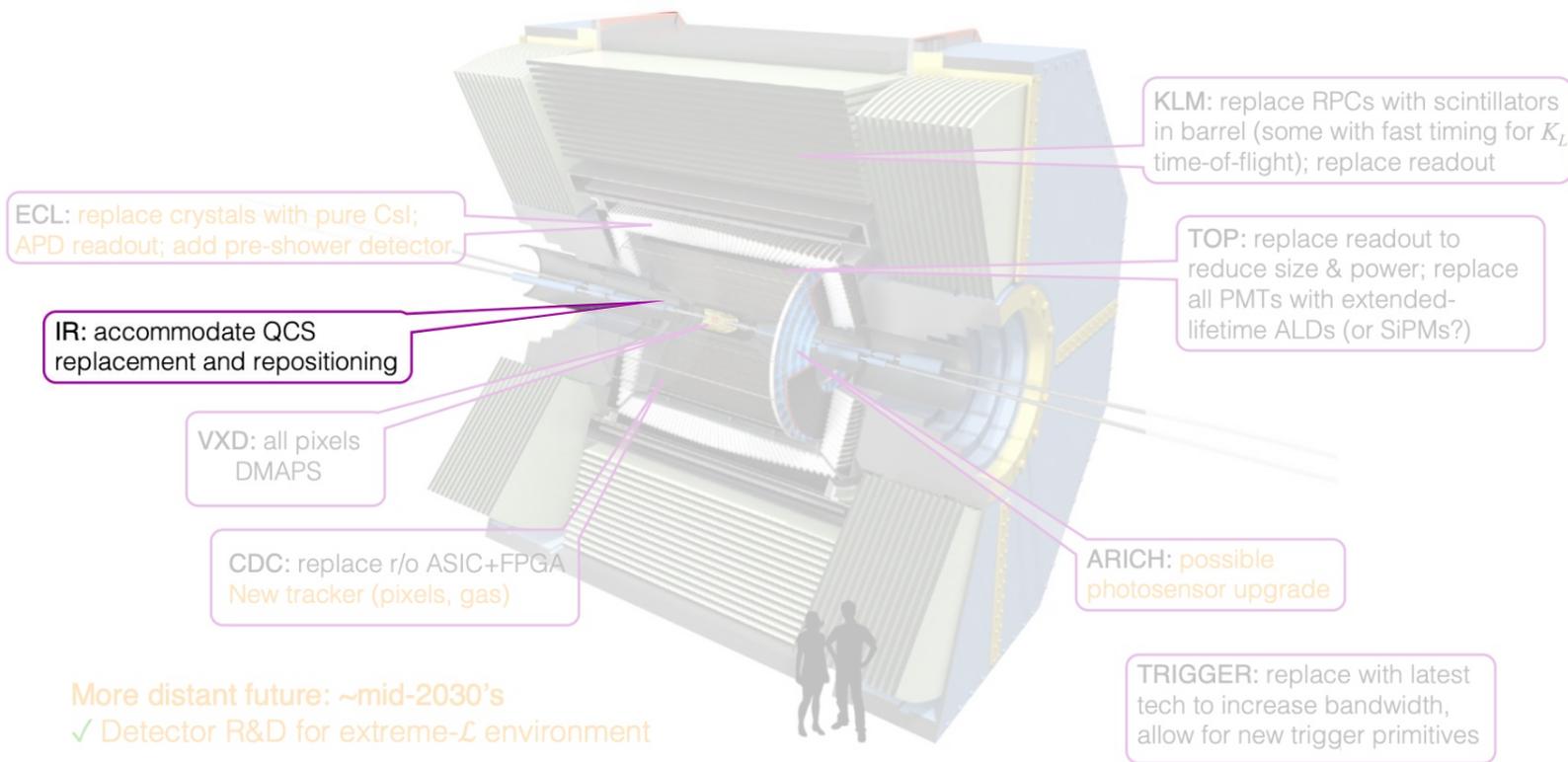
# Subdetectors and their physics impact



# LS2 and longer-term upgrades



# LS2 and longer-term upgrades



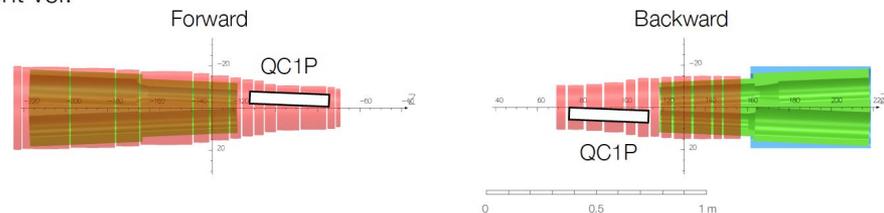
# Interaction region

## Potential upgrade

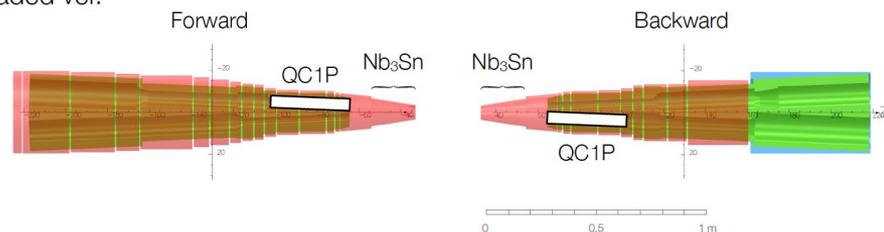
- Limit beam-beam effects, preserve beam lifetime
- Redesign final focus:
  - Extend final magnet closer to IP
  - New anti-solenoid Niobium-tin coil placed between final magnet and IP; complex R&D ongoing
  - Overall: nearly **double the Touschek lifetime** in simulations

→ *if adopted*, the envelope for inner detector services will change

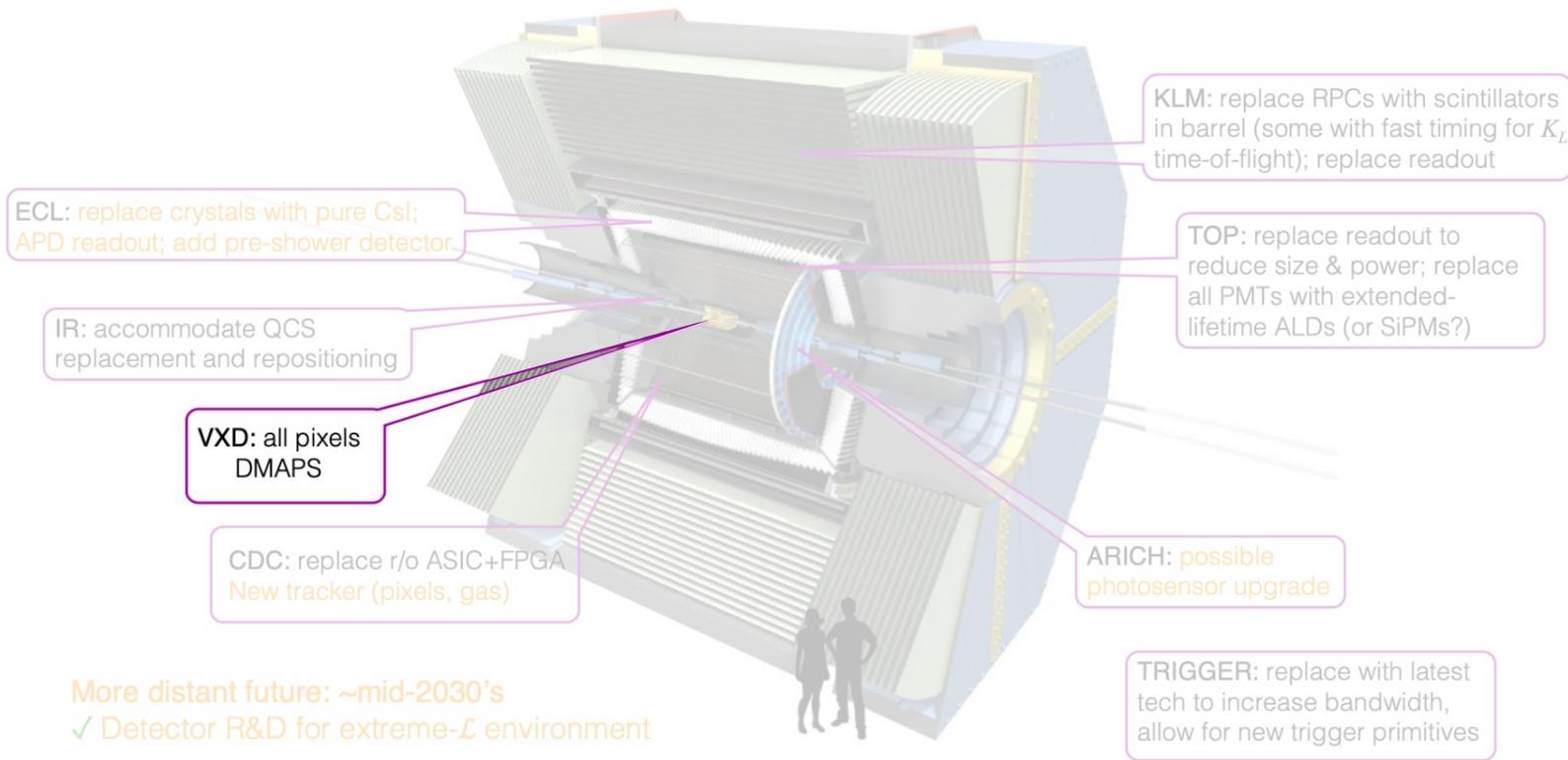
Current ver.



Upgraded ver.



# LS2 and longer-term upgrades



## VXD upgrade

### Motivation

- Handle high background rates
- *Improved* tracking and vertex resolution
- Simplify vertex system (pixels + strips → pixels)
- Contribute to L1 trigger
- Operation without data reduction

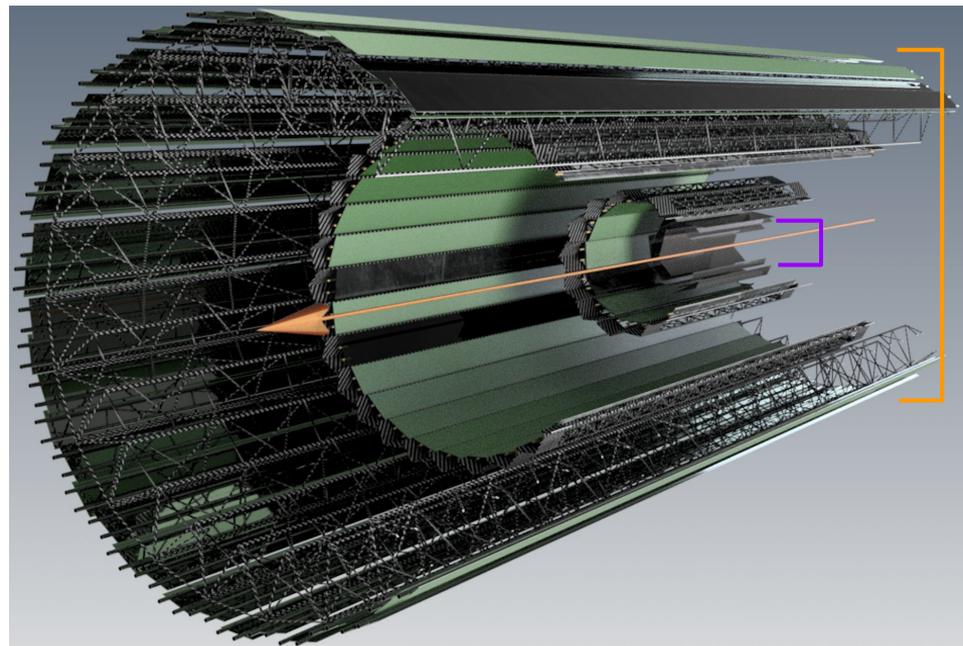
### Specifications

Chip	
Pixel pitch	30-40 $\mu\text{m}$
Integration time	$\lesssim 100$ ns
Performance	
Single-point resolution	$< 15$ $\mu\text{m}$
Material budget	0.1%–0.8% $X_0$ (inner-outer layers)
Environment	
Hit rate	120 MHz/cm <sup>2</sup>
Total ionizing dose	100 Mrad
NIEL fluence	$5 \times 10^{14}$ n <sub>eq</sub> /cm <sup>2</sup>

## VTX

### All-layer DMAPS pixel detector

- Monolithic active CMOS pixels in 5 layers
- Sensitive layer thickness  $< 50 \mu\text{m}$  ( $\sim 4000e$  from MIPs vs. 200-250e threshold)
- Sensor thickness  $< 100 \mu\text{m}$
- **iVTX**: innermost 2 layers, self-supported, air-cooled
- **oVTX**: outer 3 layers, CF structure, single-phase coolant
- Prototype (TJMonopix2) has largely met these specifications, including irradiation tests

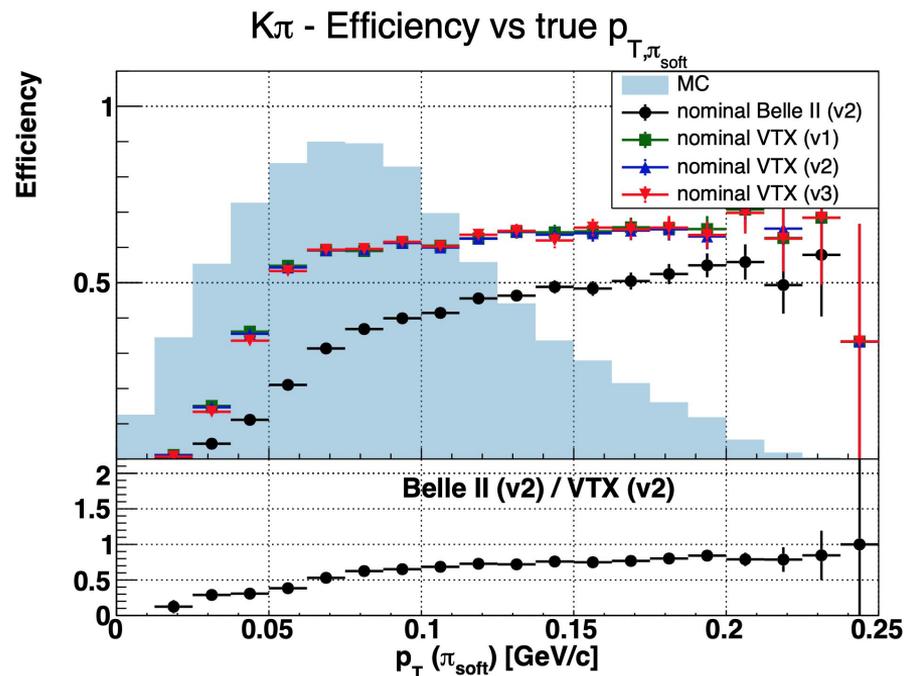


## VTX

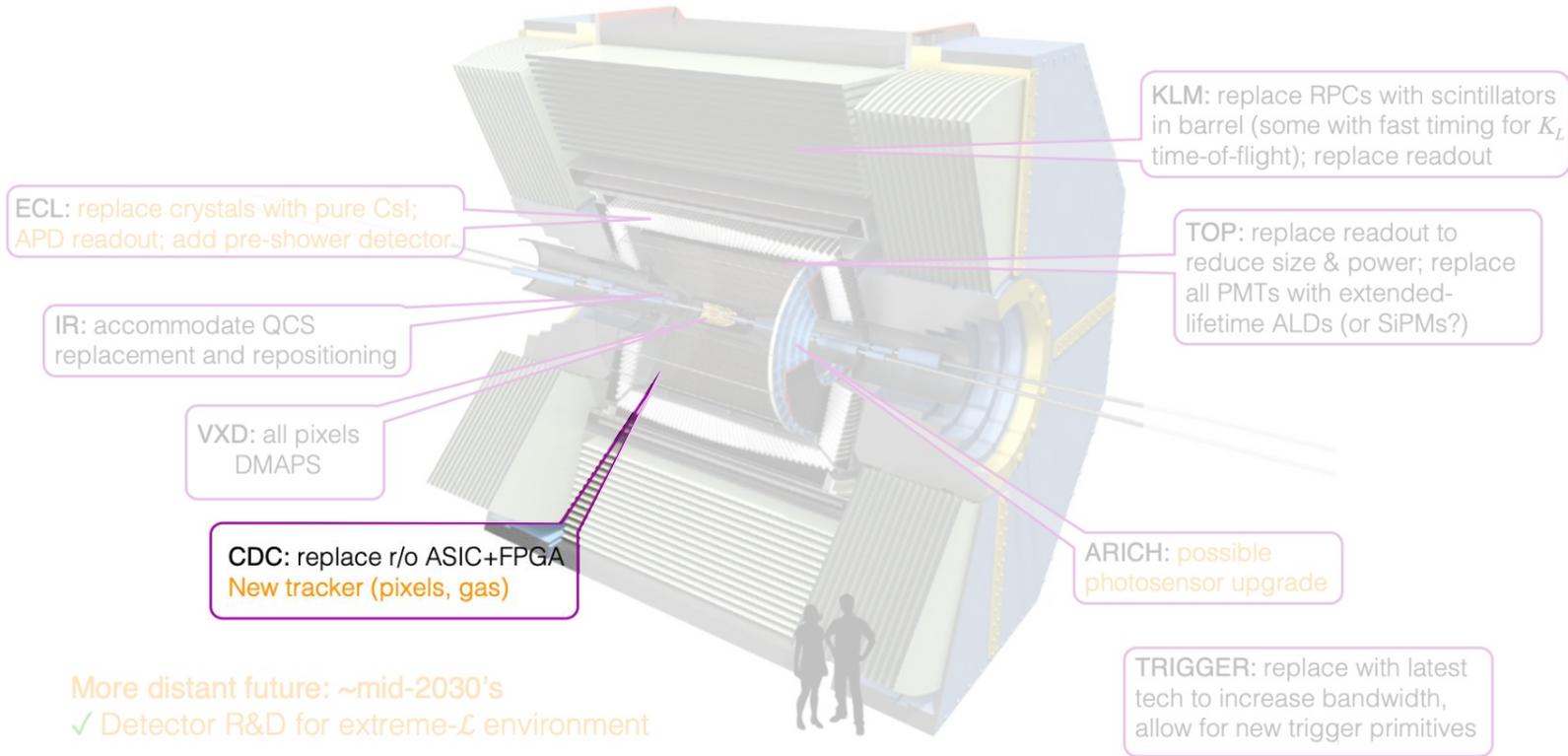
## Physics impact, illustrated

- $B^0 \rightarrow D^{*-} \ell^+ \nu$ : “bread-and-butter” physics for Belle II (R( $D^*$ ), angular analysis,  $|V_{cb}|$ ,  $B$ -tagging, ...)
- Slow pion from  $D^*$  decay: low- $p \rightarrow$  low-efficiency
- ~70% improvement in efficiency; like a massive lumi. boost
- Also: ~35% better B-decay vertex resolution

...while also being far more robust against backgrounds



# LS2 and longer-term upgrades



## CDC front-end electronics

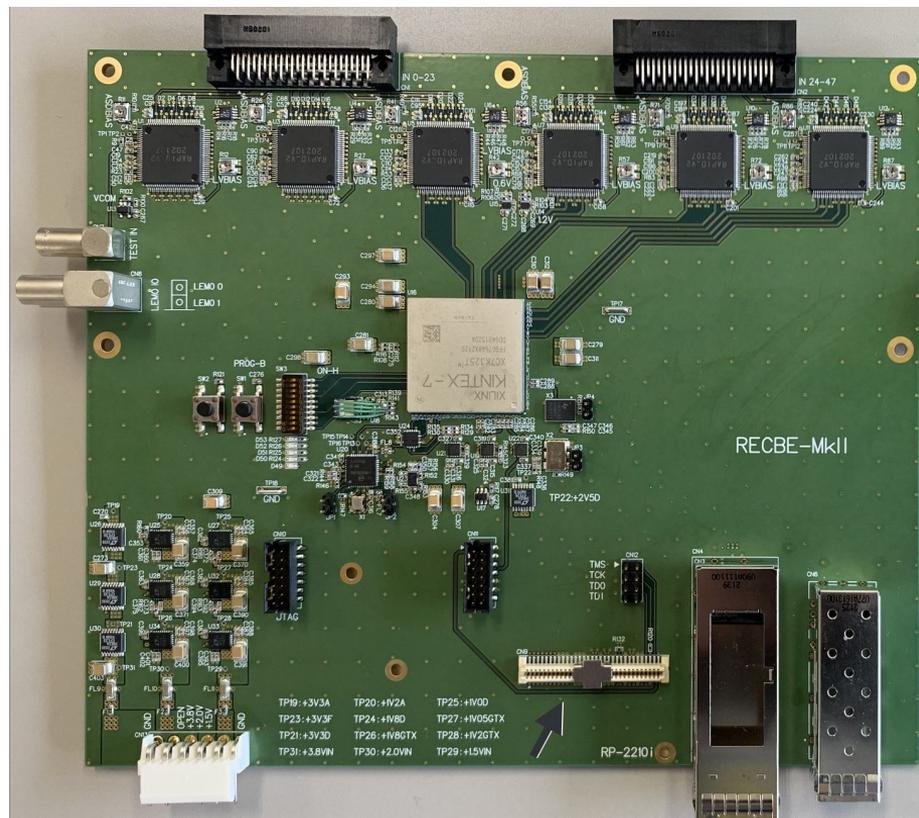
Toward better tracking performance

- Reduce cross-talk, power consumption, and increase output bandwidth
- Improve radiation tolerance

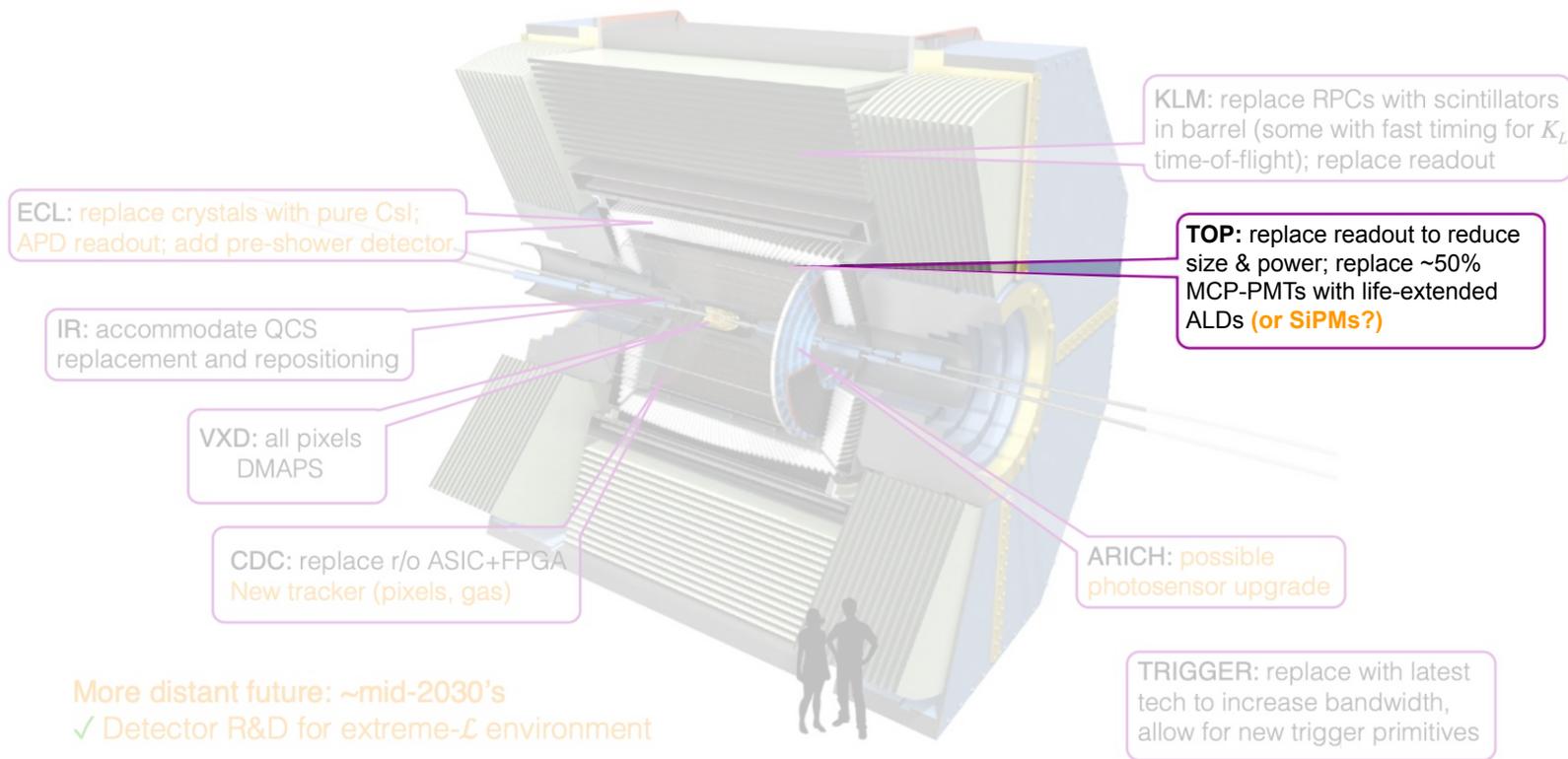
New ASICs, new FPGA, optical module

- ASIC: timing and waveform digitization
- FPGA: online data processing for trigger and DAQ
- Rad-hard fiber transceivers

Prototype front-end board upgrade



## LS2 and longer-term upgrades



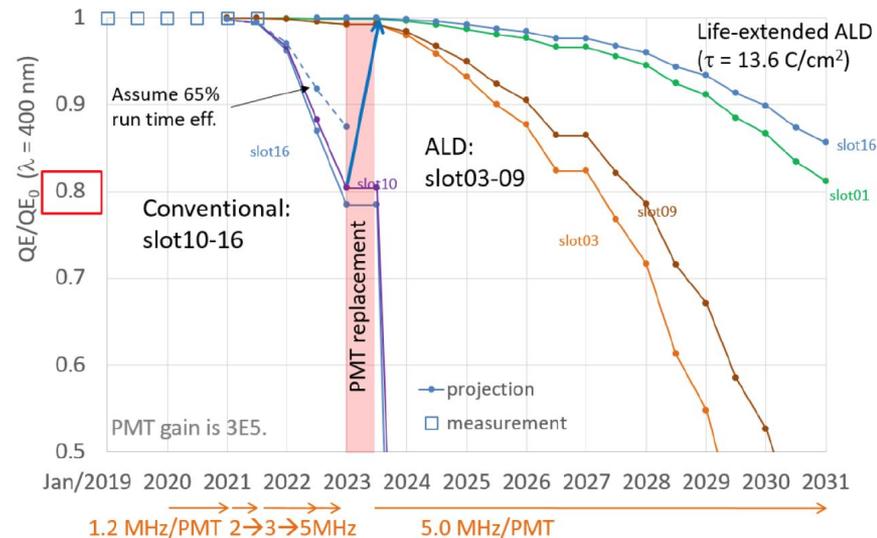
# PID: Time of Propagation

## Photosensor upgrade

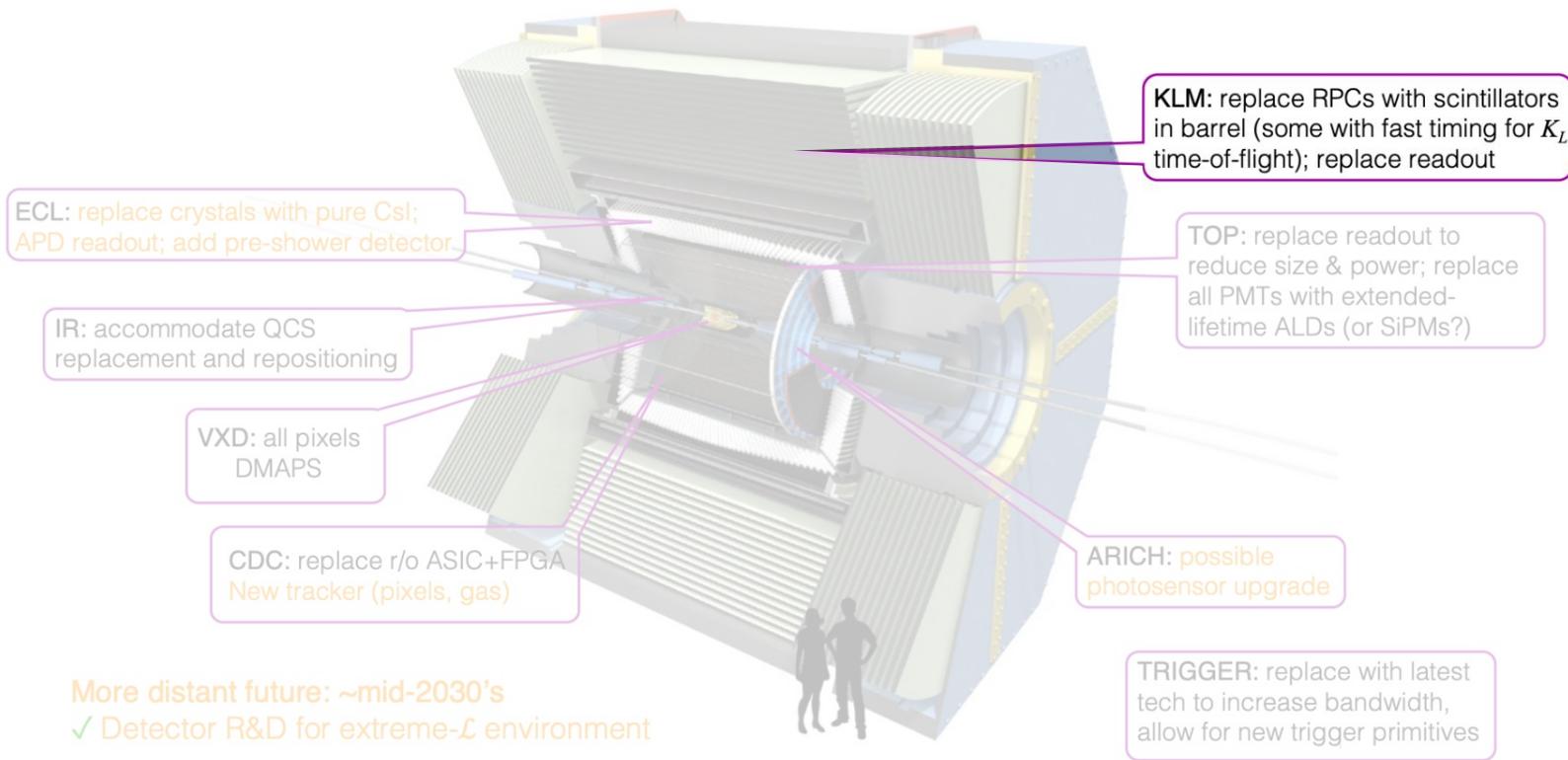
- MCP-PMTs degrading under **higher-than-expected backgrounds**
- Complete residual  $\sim 50\%$  MCP-PMT upgrade with life-extended ALD type
- (Potential replacement of MCP-PMTs with SiPMs)

## Readout upgrades

- Frontend board: reduce size and power (to accommodate potential SiPM's)
- ASoC on ASIC boards with Gpbs to FPGA



# LS2 and longer-term upgrades



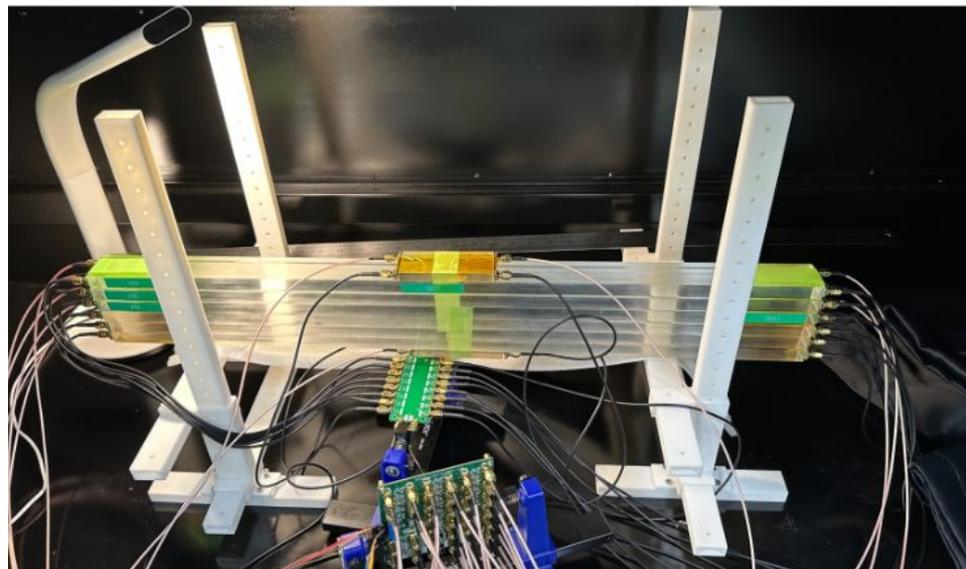
## KLM: $K_L^0$ and muon detector

New capability:  $K_L^0$  energy measurement

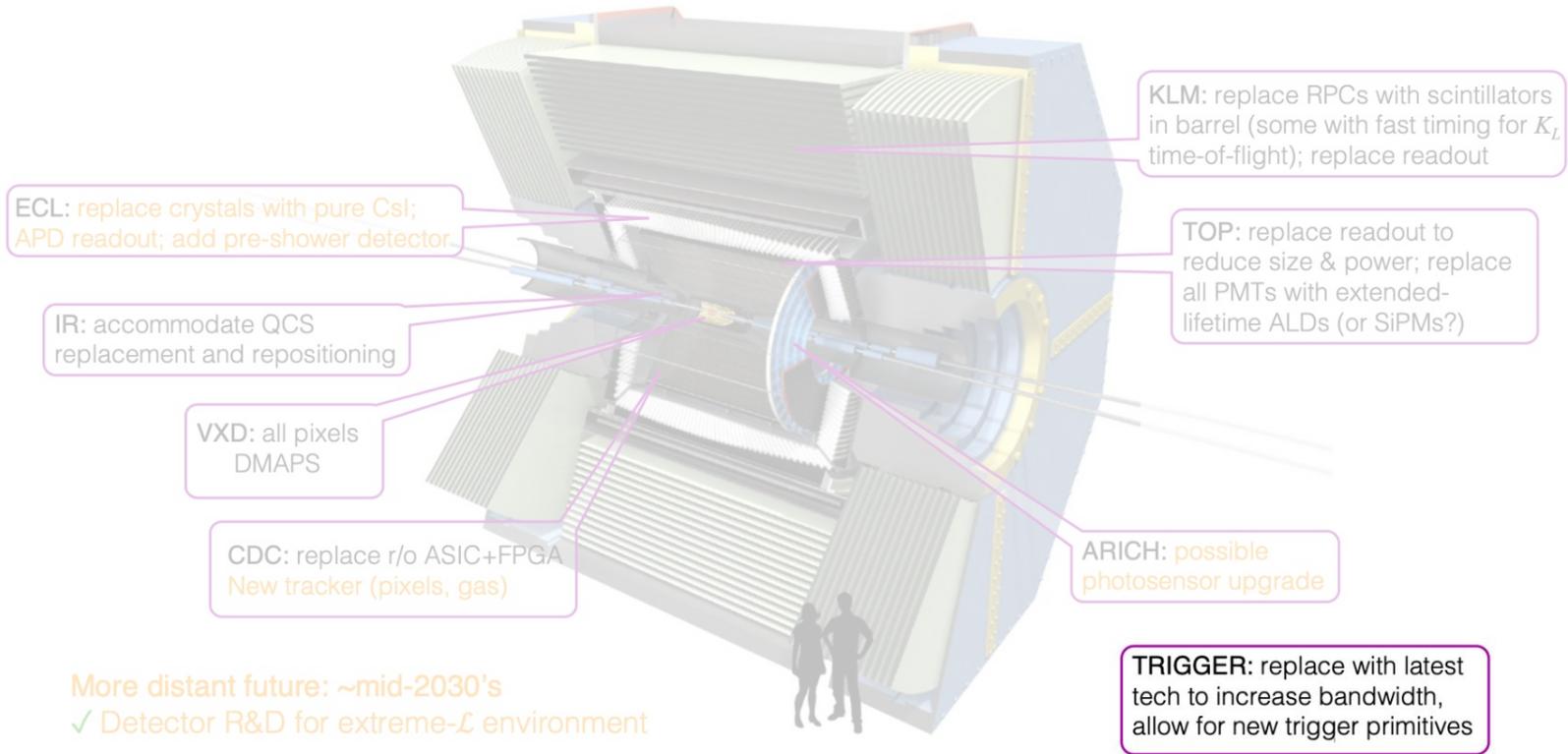
- Replace remaining RPC's with scintillators + SiPM's (very complex operation)
- **Fast timing** ( $\sim 100\text{ps}$ ) gives  $K_L^0 E$  via TOF
- Not settled: physics impact still under study

Readout upgrades

- Move feature extraction to frontend ASIC
- Replace many km of twisted-pair ribbon cables with a few fibers



# LS2 and longer-term upgrades



## LS1, LS2, and beyond

- At Belle II, **(physics output)  $\propto$  (luminosity)  $\times$  (detector performance at high lumi.)**
- Achieving **both** is an iterative process...
- ... we have a rich set of short-, medium-, and long-term upgrades in the works

Look for the Belle II Upgrades CDR soon



Thank you!